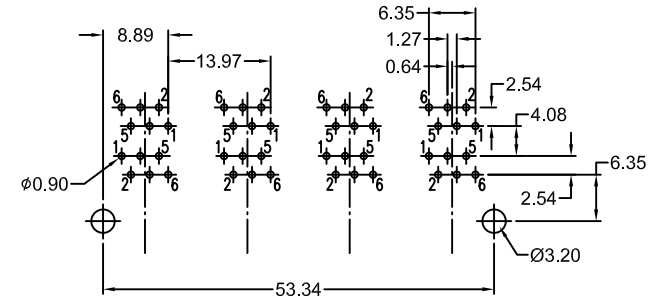
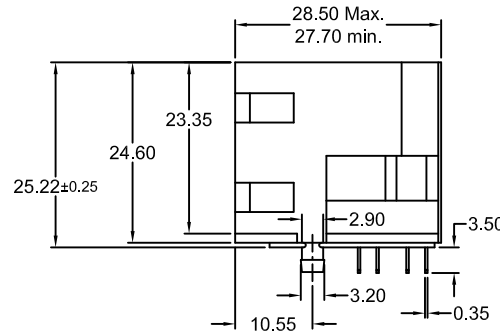
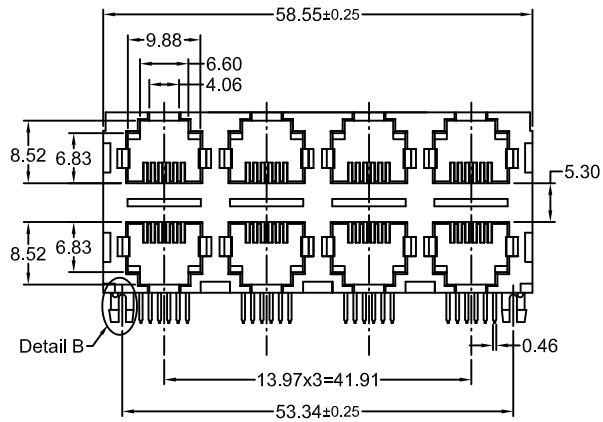
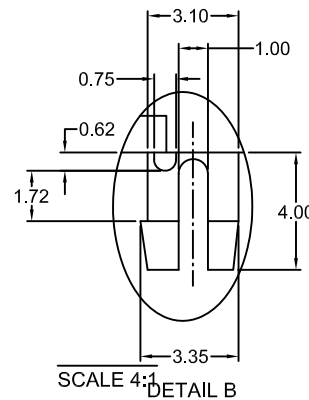


REV.	DESCRIPTION	DRAWN	DATE
A	Release	Ryan Chou	03.06.2011
B	New DWG. No.	Ronny	01.02.2012
C	Correct DWG. No. 3311504 -> 3311301	Ronny	04.07.2013



**RECOMMENDED PCB LAYOUT
TOP VIEW**



replace "x" with No. of contacts: "6" or "4"

PART NUMBER	GOLD PLATING CONTACT AREA
MJS-8T6x-S3L0-55	3μ"
MJS-8T6x-S3L0-56	6μ"
MJS-8T6x-S3L0-57	15μ"
MJS-8T6x-S3L0-58	30μ"
MJS-8T6x-S3L0-5H	50μ"

SPECIFICATIONS

Current rating: 1.5A
 Insulation resistance: 500MΩ min./500V DC
 Dielectric withstanding: 1000V rms 60Hz/Minute
 Contact resistance: 40mΩ max./20mV DC
 Operating temperature: -40°C ~ +105°C
 Insulator material: PBT UL 94V-0 color: Black
 Contact material: Phosphor Bronze
 Finish: Gold plated

Cavity confirms to FCC Rules and Registration PAR68, Subparts F



UNIT mm
 SCALE Free

GENERAL TOLERANCE	
X.° ± 3°	.X° ±
X. ±	.X0 ± 0.38
XX. ±	.XX ± 0.25
XXX. ±	.XXX ±

DRAWN Ryan Chou	DATE 03.06.2011
CHECK Ronny	DATE 04.07.2013
APPROVE Hogi	DATE 04.07.2013

DWG. NO. 3311301	SHEET 1/1
Series NO. MJS-8T6x-S3L0-xx	REV. C

STACKED GANG JACK JACK
 RJ12 <6P6C> & RJ14 <6P4C> (4x2)
 side entry - non shielded