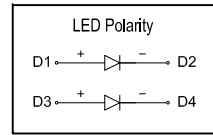
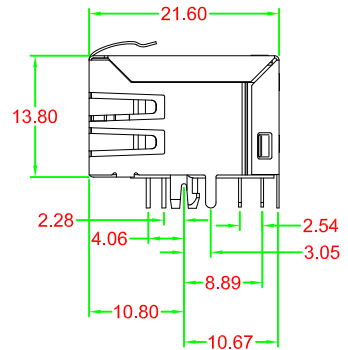
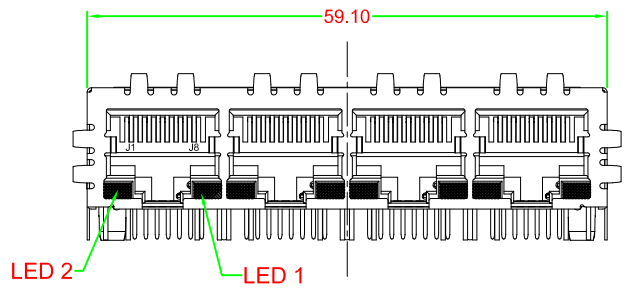
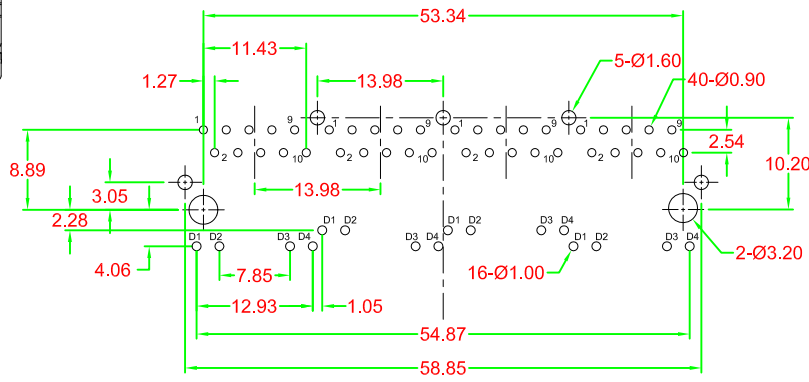
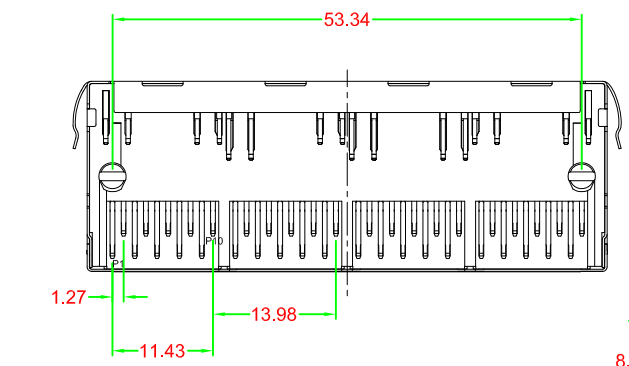
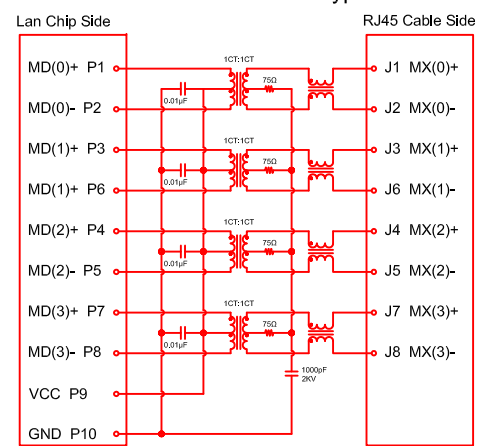


REV.	DESCRIPTION	DRAWN	DATE
A	Release	Ryan Chou	09.08.2011
B	New DWG. No.	Ronny	01.02.2012



SCHEMATIC "F" Type



ELECTRICAL CHARACTERISTICS:

Test Notes: 25°C±5°C
 TR: (100KHz, 0.1V)
 Pins: (P1,P2);(J1,J2)=1:1±3%;(P3,P6);(J3,J6)=1:1±3%
 Pins: (P4,P5);(J4,J5)=1:1±3%;(P7,P8);(J7,J8)=1:1±3%
 LX: (100KHz, 100mV, 8mA, DC Bias)
 Pins: (P1,P2),(P3,P6),(P4,P5),(P7,P8)=350µH mln.
 DCR:
 Pins: (J1-J2),(J3-J6),(J4,J5),(J7,J8)=1.2Ω max.
 HIPOT:
 Pins: (P1,P2) to (J1,J2),(P3,P6) to (J3,J6)=1500V AC for 60 sec. or 2250V DC 60 sec.
 Pins: (P4,P5) to (J4,J5),(P7,P8) to (J7,J8)=1500V AC for 60 sec. or 2250V DC 60 sec.
 INSERTION LOSS:
 -0.8dB max. at 0.3MHz to 100MHz;
 -1.2dB max. at 100MHz to 120MHz;
 RETURN LOSS:
 -18dB min. at 0.5MHz to 40MHz;
 -12+20 Log (f/80MHz)dB min. at 0.1MHz to 100MHz
 CROSS TALK:
 -33+20 Log (f/100MHz)dB min. at 0.1MHz to 100MHz
 COMMON TO DIFFERENTIAL MODE REJECTION:
 -35dB min. At 300MHz to 100MHz
 COMMON TO COMMON MODE REJECTION:
 -30dB min. At 300MHz to 100MHz

Recommended PCB Hole Layout (PCB TOLERANCE ±0.05)



SPECIFICATION

MATERIALS
 Insulator: PBT UL 94V-0
 Contact: 0.35mm Phosphor Bronze
 Plated with Gold, and Tin in solder area
 Shield: Brass, Nickel plated
 Operating Temperature: 0°C to ~+70°C

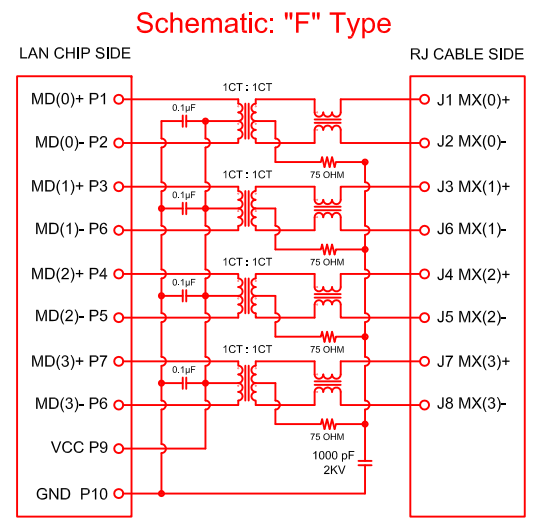
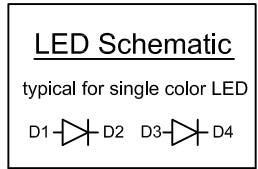
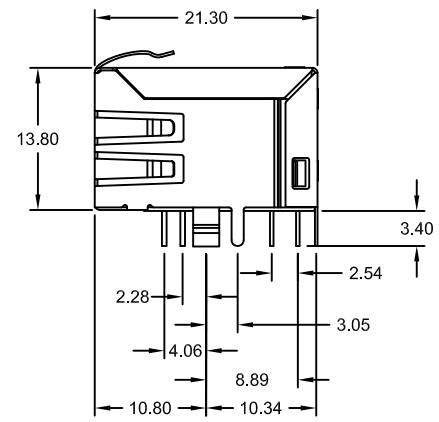
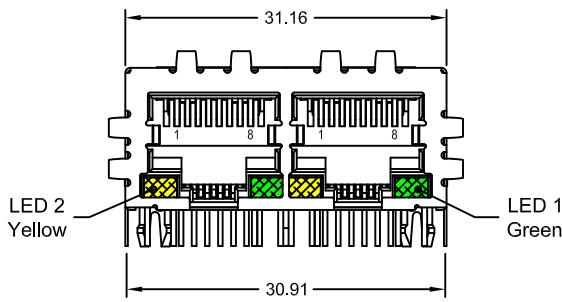
PART NUMBER	GOLD PLATING CONTACT AREA	LED Color Options	
		LED 2	LED 1
MJS-408-KOLF-95/7xCA	3µ"	YELLOW	GREEN
MJS-408-KOLF-96/7xCA	6µ"	NO LED	GREEN
MJS-408-KOLF-97/7xCA	15µ"	GREEN	NO LED
MJS-408-KOLF-98/7xCA	30µ"	GREEN	YELLOW
MJS-408-KOLF-99/7xCA	50µ"	ORANGE/GREEN	ORANGE/GREEN
MJS-408-KOLF-9H/7xCA	50µ"	YELLOW/GREEN	YELLOW/GREEN
		RED	NO LED



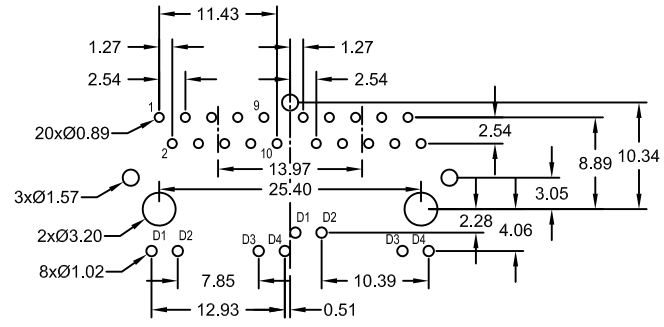
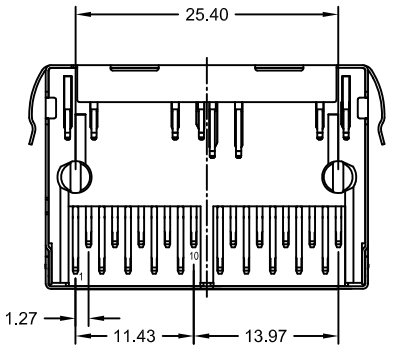
UNIT	GENERAL TOLERANCE	DRAWN	DATE	DWG. NO.	SHEET 1/1
mm	X.° ± 3° .X° ±	Ryan Chou	09.08.2011	2342600	
SCALE	X. ± .X0 ± 0.38	CHECK	DATE	Series NO.	REV. B
Free	XX. ± .XX ± 0.25	Ronny	01.02.2012	MJS-408-KOLF-9x/7xCA	
	XXX. ± .XXX ±	APPROVE	DATE		
		Hogi	01.02.2012		

Multi Port "side by side" RJ 50 PCB JACK
4x <10P/8C> "TAB DOWN"
 side entry, long profile Type with LED
 fully shielded, with Grd. Latches around
 10/100/1000 Base TX Filtered

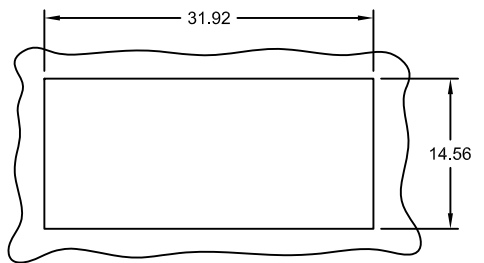
REV.	DESCRIPTION	DATE	DRAWN
A	NEW	11.07.2013	Ronny



ELECTRICAL CHARACTERISTICS:
 Test Notes: 25°C±5°C
 TR: (100KHz, 0.1V)
 Pins: (P1,P2):(J1,J2)=1:1±3%;(P3,P6):(J3,J6)=1:1±3%
 Pins: (P4,P5):(J4,J5)=1:1±3%;(P7,P8):(J7,J8)=1:1±3%
 LX: (100KHz, 100mV, 8mA, DC Bias)
 Pins: (P1,P2),(P3,P6),(P4,P5),(P7,P8)=350µH min.
 DCR:
 Pins: (J1-J2),(J3-J6),(J4,J5),(J7,J8)=1.2Ω max.
 HIPOT:
 Pins: (P1,P2) to (J1,J2),(P3,P6) to (J3,J6)
 =1500V AC for 60 sec. or 2250V DC 60 sec.
 Pins: (P4,P5) to (J4,J5),(P7,P8) to (J7,J8)
 =1500V AC for 60 sec. or 2250V DC 60 sec.
 INSERTION LOSS:
 -0.8dB max. at 0.3MHz to 100MHz;
 -1.2dB max. at 100MHz to 120MHz;
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 -12+20 Log (f/80MHz)dB min. at 01MHz to 100MHz
 CROSS TALK:
 -33+20 Log (f/100MHz)dB min. at 0.1MHz to 100MHz
 COMMON TO DIFFERENTIAL MODE REJECTION:
 -35dB min. At 300MHz to 100MHz
 COMMON TO COMMON MODE REJECTION:
 -30dB min. At 300MHz to 100MHz



Recommended PCB Layout Top View
(Tolerance ±0.08)



Recommended Panel Cutout

Specifications Materials

Contact: Phosphor Bronze t=0.35mm
 Plating: Sn in solder area;
 Au in contact area
 Insulator: PBT UL 94V-0 (Black)
 Shield: Copper Alloy t=0.25mm
 Plating: Ni
 Operating temperature: -0°C to +70°C
 Cavity conforms to FCC Rules and
 Registration PAR68, Subparts F

PART NUMBER	GOLD PLATING CONTACT AREA
MJS-208-KCLF-95/81CA	3µ"
MJS-208-KCLF-96/81CA	6µ"
MJS-208-KCLF-97/81CA	15µ"
MJS-208-KCLF-98/81CA	30µ"
MJS-208-KCLF-9H/81CA	50µ"



UNIT	SCALE	GENERAL TOLERANCE	DRAWN	DATE	DWG. NO.	SHEET 1/1
mm	Free	X.° ± 3° .X ± .XX ± .XXX ±	Ronny	11.07.2013	2342601	REV. A
		X. ± .X ± 0.28 .XX ± 0.25 .XXX ±	Hogi	11.07.2013	SERIES NO.	
			Hogi	11.07.2013	MJS-208-KCLF-9x/81CA	

Multi Port "side by side" RJ 50 PCB JACK
2x <10P/8C> "TAB DOWN"
 side entry, long profile Type with LED
 fully shielded, with Grd. Latches around
 10/100/1000 Base TX Filtered